

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of: )  
)  
Masatsugu KITADA et al. )  
) Group Art Unit: Not assigned  
Serial No.: Not Yet Assigned )  
) Examiner: Not assigned  
Filed: January 18, 2002 )  
)  
For: SEMICONDUCTOR TESTING )  
)  
APPARATUS AND METHOD FOR  
OPTIMIZING A WAIT TIME UNTIL  
STABILIZATION OF SEMICONDUCTOR  
DEVICE OUTPUT SIGNAL

J1011 U.S. PTO  
10/050160  
01/18/02  
#2

Assistant Commissioner for Patents  
Washington, DC 20231

Sir:

**INFORMATION DISCLOSURE STATEMENT UNDER 37 C.F.R. § 1.97(b)**

Pursuant to 37 C.F.R. §§1.56 and 1.97(b), applicants bring to the Examiner's attention the document listed on attached Form PTO-1449. A copy of the listed document is attached. Applicants respectfully request that the Examiner consider the document listed on attached Form PTO-1449 and indicate that it was considered by making an appropriate notation on this form.

This Information Disclosure Statement is being filed with the above-referenced application.

The following is listed on the accompanying PTO-1449 and is in a non-English language:

1. Japanese Patent Application No. 10-90347

In lieu of a statement of relevance or translation of the listed non-English language document, an English-language abstract of the document is enclosed.

This submission does not represent that a search has been made or that no better art exists and does not constitute an admission that the listed document is material or constitutes "prior art." If the Examiner applies any of the document as prior art against any claim in the application and applicants determine that the cited documents does not constitute "prior art" under United States law, applicants reserve the right to present to the office the relevant facts and law regarding the appropriate status of such document. Applicants further reserve the right to take appropriate action to establish the patentability of the disclosed invention over the listed document, should the document be applied against the claims of the present application.

If there is any fee due in connection with the filing of this Statement, please charge the fee to our Deposit Account No. 06-0916.

Respectfully submitted,

FINNEGAN, HENDERSON, FARABOW,  
GARRETT & DUNNER, L.L.P.

Dated: January 18, 2002

By: 

Richard V. Burgujian  
Reg. No. 31,744

Enclosures  
RVB/FPD/dvz

FINNEGAN  
HENDERSON  
FARABOW  
GARRETT &  
DUNNER LLP

1300 I Street, NW  
Washington, DC 20005  
202.408.4000  
Fax 202.408.4400  
www.finnegan.com

## INFORMATION DISCLOSURE CITATION

Atty. Docket No.	04329.2724	Serial No.	Not assigned
Applicant	Masatsugu KITADA et al.		
Filing Date	January 18, 2002	Group:	Not assigned

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## U.S. PATENT DOCUMENTS

Examiner Initial*		Document Number	Issue Date	Name	Class	Sub Class	Filing Date If Appropriate

## FOREIGN PATENT DOCUMENTS

	Document Number	Publication Date	Country	Class	Sub Class	Translation Yes or No
	10-90347	4/10/98	Japan			Abstract

## OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)


Examiner	Date Considered
*Examiner: Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.	
Form PTO 1449	Patent and Trademark Office - U.S. Department of Commerce